**5** Five storage devices are described in the table below.



In column 2, name the storage device being described.

In columns 3, 4, or 5, tick ( $\checkmark$ ) to show the appropriate category of storage.

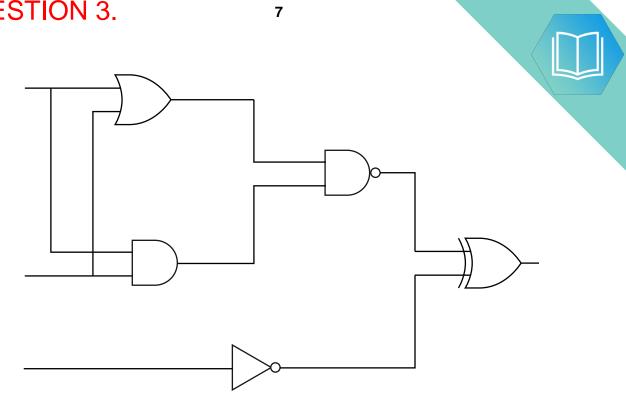
1	2	3	4	5	
Description of storage device	Name of	Category of storage			
Description of Storage device	storage device	Primary	Secondary	Off-line	
optical media which use one spiral track; red lasers are used to read and write data on the media surface; makes use of dual-layering technology to increase the storage capacity					
non-volatile memory chip; contents of the chip cannot be altered; it is often used to store the start up routines in a computer (e.g. the BIOS)					
optical media which use concentric tracks to store the data; this allows read and write operations to be carried out at the same time					
non-volatile memory device which uses NAND flash memories (which consist of millions of transistors wired in series on single circuit boards)					
optical media which use blue laser technology to read and write data on the media surface; it uses a single 1.1 mm polycarbonate disc					

Che	eck digits are use	ed to e	nsure	the ac	curacy (	of ente	ed dat	a.			
A 7	-digit number ha	s an e	xtra di	igit on t	he right	t, called	I the ch	eck dig	it.		
	digit position	: 1		2	3	4	5	6	7	' 8	3
	digit:	-		_	-	_	_	-	-	. <u>-</u>	<u> </u>
										check	k digit
The	e check digit is ca	alculate	ed as	follows	:						
•	each digit in the the seven resu this total is divi the remainder	lts are ded by	then a	added t	ogether	,			check o	ligit is X	)
(a)	Calculate the c	heck d	ligit fo	r the fol	llowing	numbe	r. Show	all you	r worki	ng.	
		4	2	4	1	5	0	8			
	Check digit										
											[2
(b)	An operator ha	s just l	keyed	in the f	ollowing	g numb	er:				
		3	2	4	0	0	4	5	X		
	Circle below co	orrect	if the o	check c	ligit is c	orrect (	OR inc	orrect i	f the ch	neck dig	it is incorrect.
				corre	ect	inco	rrect				
	Explain your ar	nswer.									

[3]



(a)



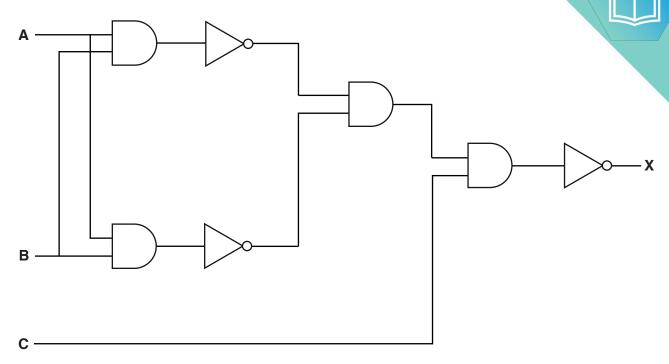
Complete the truth table for this logic circuit.

Α	В	С	Working space	х
0	0	0		
0	0	1		
0	1	0		
0	1	1		
1	0	0		
1	0	1		
1	1	0		
1	1	1		

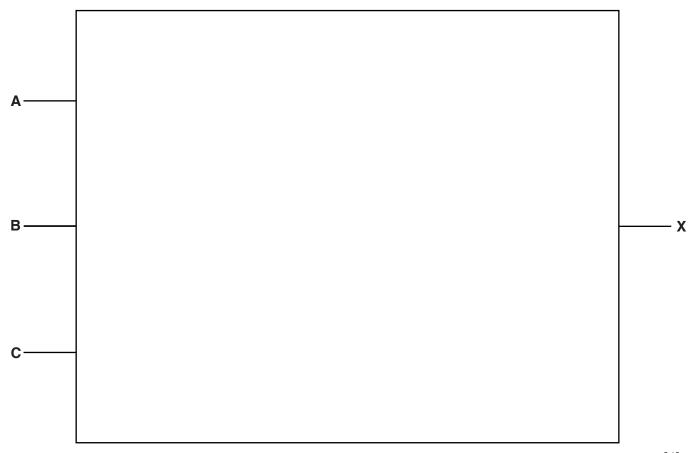
s 1 OR B is 1 ) .	 	,

[5]

(c) Re-draw the following logic circuit using NAND gates only.



Logic circuit re-drawn:

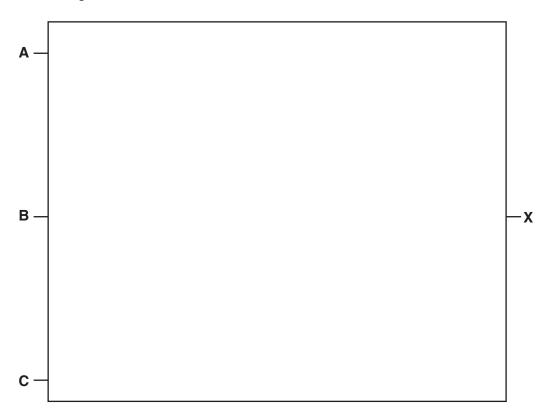


10 For this logic statement:

X = 1 if (( A is 1 AND B is 1 ) OR ( B is 1 AND C is NOT 1 ))



(a) Draw the logic circuit.



[4]

**(b)** Complete the truth table for the given logic statement.

A	В	С	Working space	Х
0	0	0		
0	0	1		
0	1	0		
0	1	1		
1	0	0		
1	0	1		
1	1	0		
1	1	1		

[4]

# QUESTION 5.

7

7 Consider the logic statement:



X = 1 if ((A is 1 AND B is NOT 1) NAND C is 1) XOR ((A is 1 AND C is 1) OR B is

(a) Draw a logic circuit to represent the given logic statement.



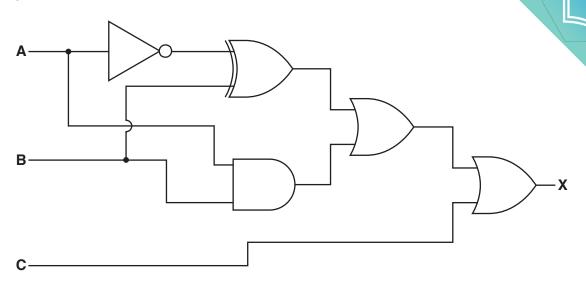
**(b)** Complete the truth table for the given logic statement.

A	В	С	Working space	х
0	0	0		
0	0	1		
0	1	0		
0	1	1		
1	0	0		
1	0	1		
1	1	0		
1	1	1		

# **QUESTION 6.**

8

8 A logic circuit is shown below.



(a) Complete the truth table for the given logic circuit.

A	В	С	Working space	Х
0	0	0		
0	0	1		
0	1	0		
0	1	1		
1	0	0		
1	0	1		
1	1	0		
1	1	1		

**(b)** Draw a logic circuit corresponding to this logic statement:

X = 1 if (A is NOT 1) OR ((B is 1 OR C is 1) AND (B is NOT 1 OR A is NOT 1))



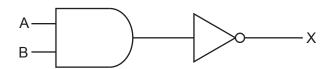
### QUESTION 7.

4

4 (a) Identify the name and draw the single logic gate that can replace the given



(i)

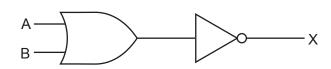


Name of gate: .....

Drawing of gate:



(ii)



Name of gate: .....

Drawing of gate:

[2]

**(b)** Complete the truth table for the given logic statement:

$$X = (((A OR C) AND (NOT A AND NOT C)) XOR B)$$

Α	В	С	Working space	Х
0	0	0		
0	0	1		
0	1	0		
0	1	1		
1	0	0		
1	0	1		
1	1	0		
1	1	1		

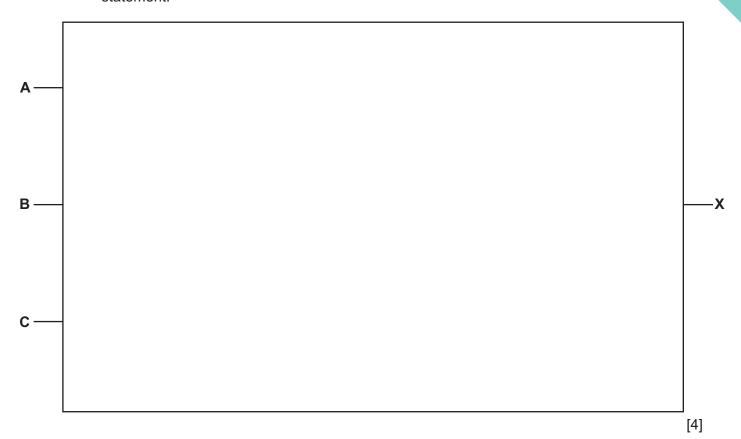
**6** Consider the given logic statement:



#### X = (((A XOR B) AND C) OR NOT C)

(a) Draw a logic circuit to match the given logic statement.

All logic gates must have a maximum of **two** inputs. Do **not** attempt to simplify the logic statement.



**(b)** Complete the truth table for the given logic statement.

A	В	С	Working space	Х
0	0	0		
0	0	1		
0	1	0		
0	1	1		
1	0	0		
1	0	1		
1	1	0		
1	1	1		

(a) (i) Convert the following two hexadecimal numbers into binary:

FA7
D3E

FA7
D3E

(ii) Now perform the AND (logic) operation on each corresponding pair of binary bits in the two numbers from part (i).

[2]

(iii) Convert your answer in part (ii) into hexadecimal.

[2]

(b)	(i)	The following code shows HTML 'tag' pairs on either side of the text's that each creates.	
		<fort "="" #="" 0="" color="" f=""> RED </fort> <fort "="" #="" 0="" color="" f=""> GREEN </fort> <fort "="" #="" 0="" color="" f=""> BLUE </fort>	
		<pre><font "="" #<="" color="" th=""><th></th></font></pre>	
		Yellow is a combination of red and green, magenta a combination of red and blue are cyan a combination of green and blue.	ıd
		State what 6-digit hexadecimal values should replace X, Y and Z in the above code.	
		X	
		Y	
		Z	 [3]
	(ii)	Describe how other colours, such as a darker shade of blue, are created.	o]
	( )		
			 [2]
(c)	1A -	– 16 – C5 – 22 – FF – FF is an example of a MAC address.	
	(i)	Identify what the first six and last six hexadecimal digits represent.	
		First six digits	
		Last six digits	
	<b></b> \		2]
	(ii)	State why MAC addresses are used.	
			 1]

### **QUESTION 10.**

6

Three switches, A, B and C, each send values of 0 or 1 to a logic circuit. Value X logic circuit.





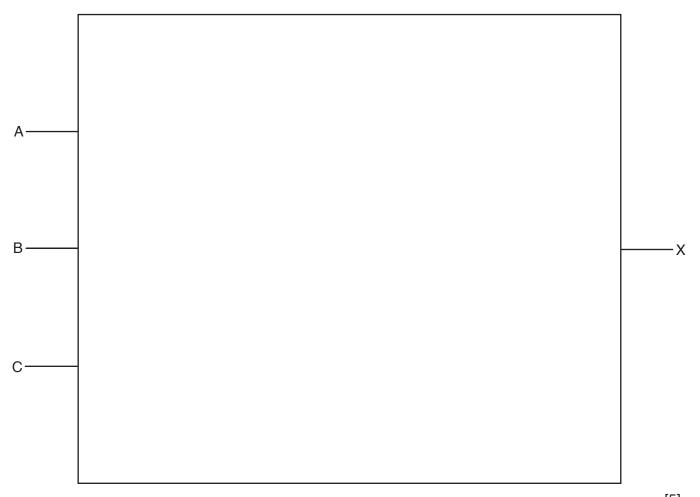
Output X has a value of 1 depending on the following conditions:

Switch A sends value 1 AND Switch B sends value 0

OR

Switch B sends value 1 AND Switch C sends value 0

(a) Draw a logic circuit to represent the conditions above.



**(b)** Complete the truth table for the conditions given at the start of question 5.

			Working on oo	
Α	В	С	Working space	X
0	0	0		
0	0	1		
0	1	0		
0	1	1		
1	0	0		
1	0	1		
1	1	0		
1	1	1		

[4]

(c) A microprocessor regularly samples the output, X. Each sample value is stored in an 8-bit register as shown below. One bit of this register is reserved as a parity bit.

Five consecutive output values of 1 indicate a fault condition.

Identify which of the following registers shows a fault condition.

Parity b	it							
1	1	1	1	1	0	0	1	Register Y
0	1	0	1	1	1	1	1	Register Z

Dogictor	 ГН	٦
negistei	 LI	J

(d) When eight bytes of data have been collected, they are transmitted to a caway. Parity checks are carried out to identify if the data has been transmitted system uses **even parity** and column 1 is the parity bit.



The eight bytes of data are sent together with a ninth parity byte:

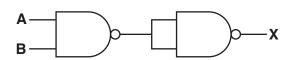
	parity bit	column 2	column 3	column 4	column 5	column 6	column 7	column 8
byte 1	1	0	0	0	0	1	0	0
byte 2	1	1	1	1	0	0	1	1
byte 3	0	1	0	0	1	0	0	0
byte 4	0	1	1	1	0	0	0	1
byte 5	1	0	0	0	1	1	1	1
byte 6	0	0	0	0	0	0	0	0
byte 7	1	1	1	0	1	0	0	0
byte 8	1	0	0	0	1	1	1	0
parity byte	1	0	1	1	0	1	1	1

(i)	Identify which of the eight bytes contains an error.
	byte[1]
(ii)	Identify which column contains an error.
	column[1]
(iii)	The incorrect bit is indicated where the byte number and column cross.
	Give the corrected byte.
	[1]
(iv)	Calculate the denary value of the corrected byte.
	[1]
(v)	Considering the fault condition given in <b>part (c)</b> , explain why it is very important that the incorrect bit is located and corrected.
	[2]

7 (a) Complete the truth tables and name the single logic gate that could replace



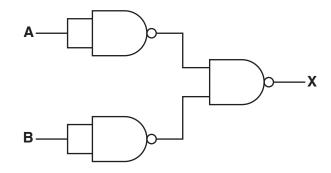
(i)



Α	В	Working space	
0	0		
0	1		
1	0		
1	1		

Single logic gate ......[3]

(ii)



A	В	Working space	х
0	0		
0	1		
1	0		
1	1		

Single logic gate .....[3]

**(b) (i)** Draw a logic circuit to represent the following logic statement:





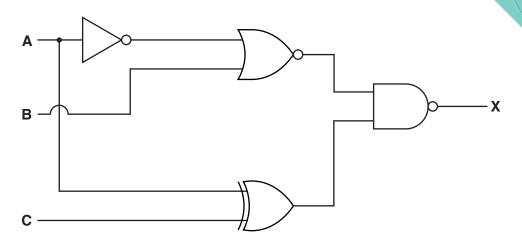


[4]

(ii) Complete the truth table for the logic statement in part (b)(i).

Α	В	С	Working space	х
0	0	0		
0	0	1		
0	1	0		
0	1	1		
1	0	0		
1	0	1		
1	1	0		
1	1	1		

3 A logic circuit is shown:



(a) Complete the truth table for the given logic circuit.

Α	В	С	Working space	X
0	0	0		
0	0	1		
0	1	0		
0	1	1		
1	0	0		
1	0	1		
1	1	0		
1	1	1		

(b)	Explain the difference between the functions of an AND gate and an OR gate.	
		[0]

[4]

# **QUESTION 13.**

6

7

(a)	Many programmers write computer programs in high-level languages. The p. be translated into machine code to be read by the computer.
	State <b>two</b> types of translator that can be used.
	Translator 1
	Translator 2
	[2
(b)	Explain <b>two</b> reasons why a computer programmer may choose to write a program in a high level language, rather than a low-level language.
	Reason 1
	Reason 2

(c) Three examples of computer code are given in the table.

Tick  $(\checkmark)$  to show whether each example of computer code is **High-level language**, **Assembly language** or **Machine code**.

Computer code	High-level language (✓)	Assembly language (✓)	Machine code (√)
10110111 11001100 01011100			
FOR X = 1 TO 10  PRINT X  NEXT X			
INP X STA X LDA Y			

[4]

9

7 A factory manufactures plastic pipes. It uses logic circuits to control the manufactures



(a) Consider the logic gate:

Complete the truth table for this logic gate.

Input A	Input B	Output
0	0	
0	1	
1	0	
1	1	

**(b)** Consider the truth table:

Input A	Input B	Output
0	0	0
0	1	1
1	0	1
1	1	0

State the <b>single</b> is	ogic gate that prod	duces the given of	utput.	

[1]

10

(c) Plastic pipes of various sizes are manufactured by heating the plastic and us



The manufacturing system uses sensors to measure the pressure (P), temperate speed (S) of production.

The inputs to the manufacturing system are:

Input	Binary value	Condition
P	1	pressure is > 5 bar
P	0	pressure is <= 5 bar
т	1	temperature is > 200 degrees Celsius
l	0	temperature is <= 200 degrees Celsius
	1	speed is > 1 metre per second
S	0	speed is <= 1 metre per second

The system will sound an alarm (X) when certain conditions are detected.

The alarm will sound when:

Temperature is > 200 degrees Celsius and the pressure is <= 5 bar or Speed is > 1 metre per second and Temperature is <= 200 degrees Celsius

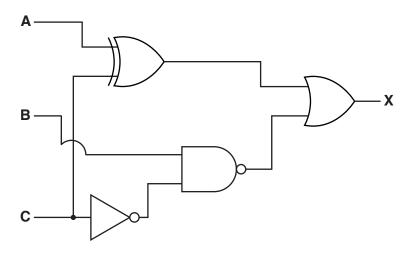
Draw a logic circuit to represent the above alarm system.

Logic gates used must have a maximum of **two** inputs.



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	11	
(d)	Give <b>two</b> benefits of using sensors to monitor the manufacture of plastic pipe	
	1	
	2	

8 Consider the logic circuit:



(a) Write a logic statement to match the given logic circuit.

.....[3]

(b) Complete the truth table for the given logic circuit.

A	В	С	Working space	Х
0	0	0		
0	0	1		
0	1	0		
0	1	1		
1	0	0		
1	0	1		
1	1	0		
1	1	1		

### **QUESTION 16.**

4 A factory that manufactures cleaning products has a system that monitors condithe manufacturing process.



The inputs to the system are:

Input	Binary value	Condition
A	1	pH > 7
A	0	pH < = 7
т	1	Temperature < 35 °C
•	0	Temperature > = 35 °C
P	1	Pressure > = 80 %
P	0	Pressure < 80 %

(a) The system will sound an alarm (X) when certain conditions are detected.

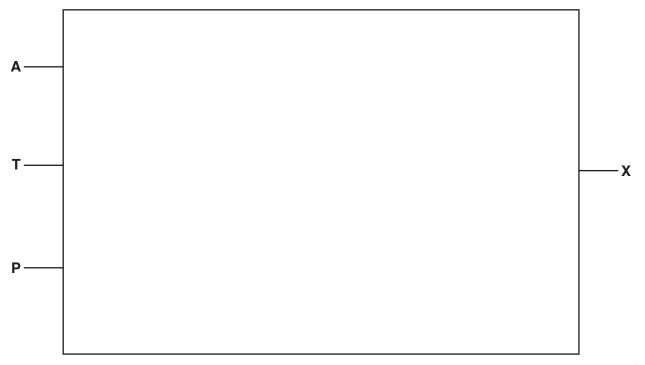
The alarm will sound when:

• The pressure > = 80 % and the temperature > = 35 °C

or

• The temperature < 35 °C and the pH > 7

Draw a logic circuit to represent the alarm system in the factory. Each logic gate must have a maximum of two inputs.



[4]

9

**(b)** Complete the truth table for the given logic problem.

A	Т	Р	Working space	X
0	0	0		
0	0	1		
0	1	0		
0	1	1		
1	0	0		
1	0	1		
1	1	0		
1	1	1		

(c) A sensor and a microprocessor are used to monitor the pH of the cleaning products. The system records each reading that is taken. If the reading is greater than 7 a warning message is displayed on a monitor.

Explain how the sensor and microprocessor are used in the system.